

ISSUE CLASSIFICATION	
Class	Subclass

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PATENT 1

U.S. UTILITY Patent Application

PD O.I.P.E. SCANNED <u>En</u> <u>5</u> Q.A. <u>Cc</u>	PATENT DATE
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APPLICATION NO. 09/511986	CONT/PRIOR	CLASS 264	SUBCLASS	ART UNIT 1732	EXAMINER N
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ITS · Vernon Williams

APPL
LE
Stereolithographically fabricated conductive elements,
semiconductor device components and assemblies including such
conductive elements, and methods

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
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	_____ (Date)		Amount Due	Date Paid
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